

Module Status - CDFG

- **Due 3/26:** “CDFG module and feedhorn designs”:

- Nearly done for dark tests (see right); remaining iteration on readout design, magnetic shielding, and verifying fit in Fermilab, SLAC, UIUC cryostats (drawings incomplete)

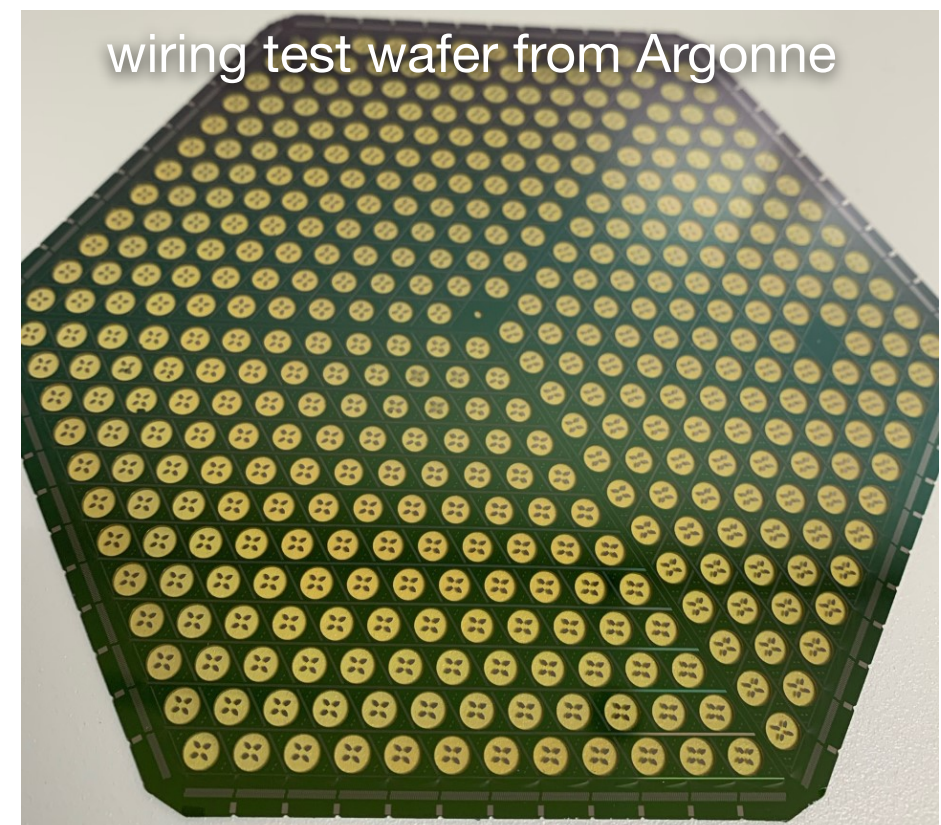
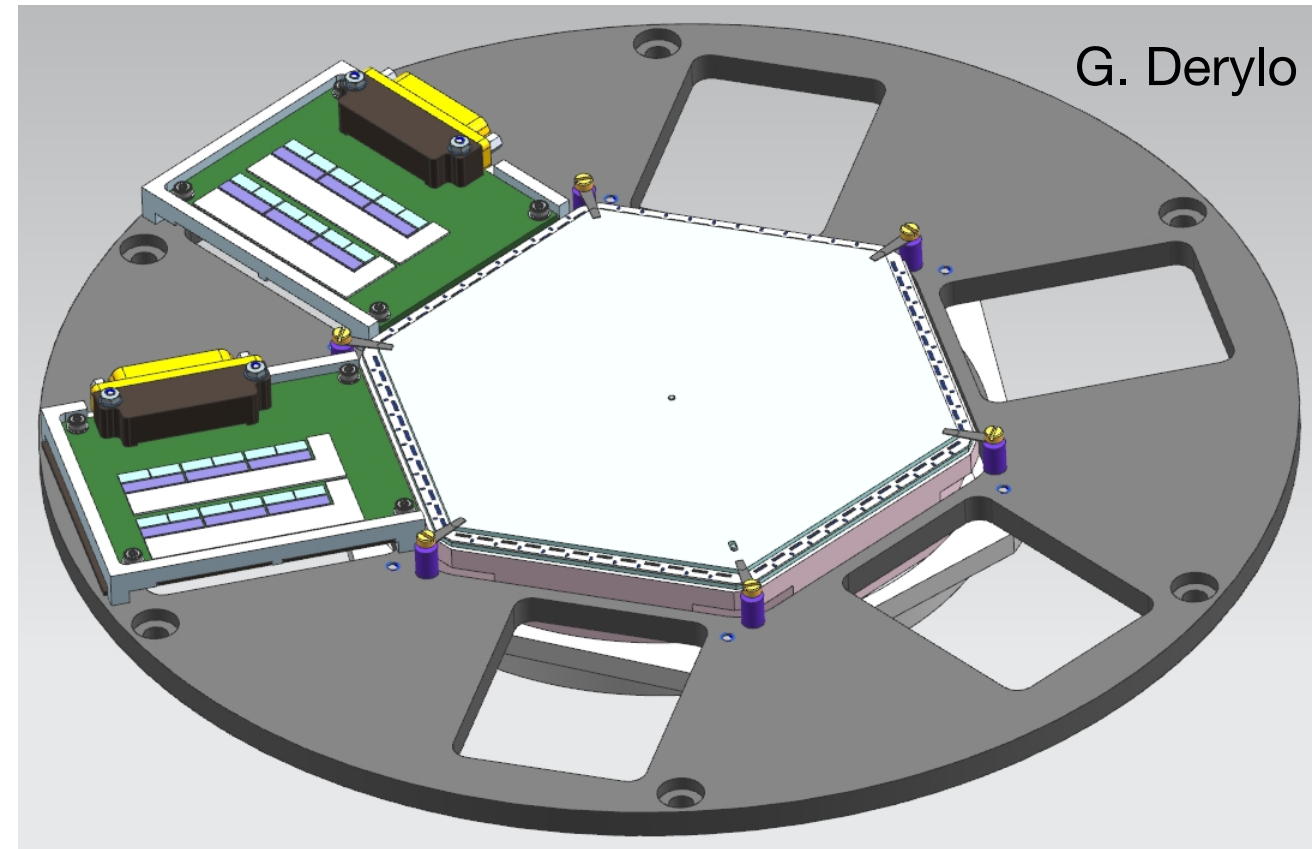
- **Due 6/18:** “Procure”... “Receive prototype parts...”

- **Due 7/2:** “Test assembly of detector module with dummy detector wafer, including 4K cryogenic mechanical test”:

- “Dummy wafer” received at Fermilab this week from Argonne (see right)
- Plan a series of wirebonding tests while waiting for CDFG module parts

- **Test cryostat commissioning progress:**

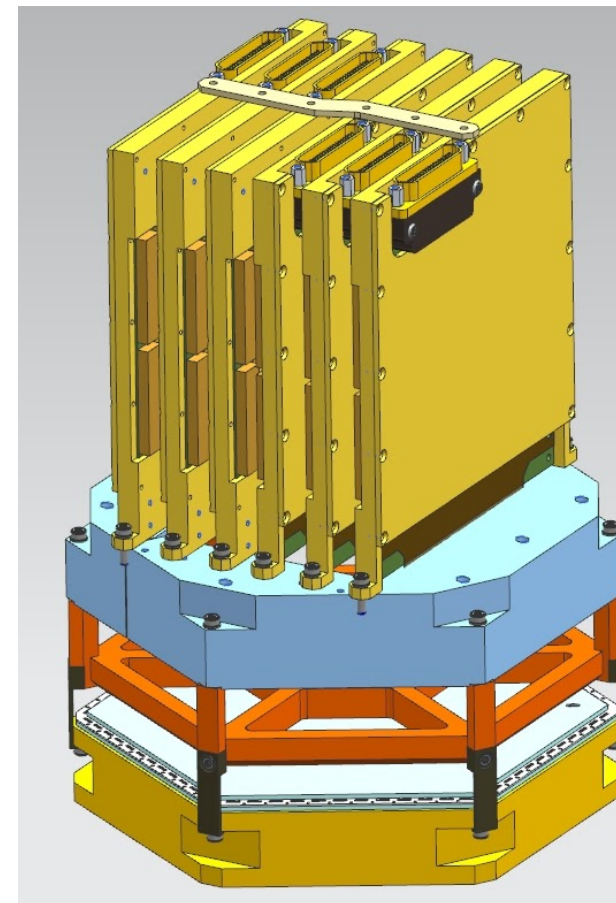
- UIUC fridge delivered recently
- Fermilab fridge delivered recently and installation starting tomorrow



Module Status - Production

- Two design concepts (“v1” and “v2”) have been developed for production module
- Making further progress requires more detailed specification of wafer dimensions for all bands and LAT/SAT to define available space
- Space requirements are quite tight, and SAT MF sensitivity requirements require careful minimization of dead space between modules to minimize wafer pitch
- Basic wafer layout needs to be joint optimization across detectors, readout, modules, SAT, and LAT

v1 module



v2 module

